

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

09/03/117

In re the Application of:

Takashi KOBAYASHI et al

Intl. Appln. No.: PCT/JP00/06146

Intl. Filing Date: 8 September 2000

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND PROCESS
FOR PRODUCING THE SAMEINFORMATION DISCLOSURE STATEMENTAssistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO-1449 are hereby cited.

Documents AQ-AS on the attached list are cited in the specification, on pages 1 and 3, and their relevance is indicated therein.

Japanese Documents AL-AO were cited in the International Search Report (copy enclosed), as was Document AA.

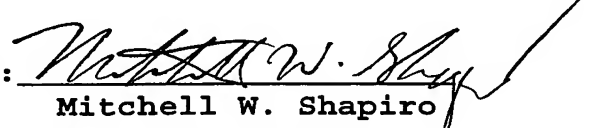
Regarding Japanese Document AP, see U.S. counterpart Document AC.

Respectfully submitted,

MWS:lmb

Miles & Stockbridge P.C.
1751 Pinnacle Drive
Suite 500
McLean, Virginia 22102-3833
(703) 903-9000

By:


Mitchell W. Shapiro
Reg. No. 31,568

January 16, 2002